Research Article

Time-Modulated Transmissive Programmable Metasurface for Low Sidelobe Beam Scanning

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Programmable metasurfaces have great potential for the implementation of low-complexity and low-cost phased arrays. Due to the difficulty of multiple-bit phase control, conventional programmable metasurfaces suffer a relatively high sidelobe level (SLL). In this manuscript, a time modulation strategy is introduced in the 1-bit transmissive programmable metasurface for reducing the SLLs of the generated patterns. After the periodic time modulation, harmonics are generated in each reconfigurable unit and the phase of the first-order harmonic can be dynamically controlled by applying different modulation sequences onto the corresponding unit. Through the high-speed modulation of the real-time periodic coding sequences on the metasurface by the programmable bias circuit, the equivalent phase shift accuracy to each metasurface unit can be improved to 6-bit and thus the SLLs of the metasurface could be reduced remarkably. The proposed time-modulated strategy is verified both numerically and experimentally with a transmissive programmable metasurface, which obtains an aperture efficiency over 34% and reduced SLLs of about −20 dB. The proposed design could offer a novel approach of a programmable metasurface framework for radar detection and secure communication applications.

1. Introduction

Metasurfaces have been extensively researched over recent years, owing to their extraordinary talents for the fine control over electromagnetic (EM) scattering [1–5]. Based on the advantages of low profile, low cost, and high integrity, metasurfaces have shown versatile potentials in many areas, such as dynamic scattering modulation [6, 7], orbital angular momentum (OAM) beam generation [8–10], direct information transmission [11], energy harvesting [12], direction finding [13, 14], and intelligent sensing [15, 16]. Particularly, programmable metasurfaces have demonstrated their capacities in phase modulation for flexible beam forming through employing lumped components, which have exhibited significantly lower cost over conventional phased arrays using digital or analog phase shifters [17].

Programmable metamaterials were firstly proposed in 2014 to expand the concept of metamaterials through using dynamic sequences of “0” and “1” bits [18]. Thereafter, reflective or folded reflective programmable metasurfaces have been widely developed and exploited in many applications, through integrating lumped components for the dynamic alteration in metasurface unit resonant property [19–23]. Since there were serious feed blockages exhibited by the programmable reflective metasurfaces, transmissive programmable metasurfaces were proposed to improve the total
radiation efficiency [24–28]. However, for all these programmable metasurfaces, only 1-bit or 2-bit phase resolutions are realized for the units, resulting in severe quantization losses of approximately 3 dB and 0.6 dB, respectively [29]. As for programmable metasurfaces with multiple-bit phase resolutions, the corresponding control circuits would be extremely complicated for practical applications, especially in a large-scale metasurfaces. To achieve a more sophisticated EM manipulation, the time-modulated array technologies [30–32] were introduced and combined with programmable metasurfaces, and the space-time coding digital metasurfaces were thus proposed and verified to provide flexible beam steering, beam shaping, and scattering-signature control over optimized space-time coding sequences [33–35]. Many other promising applications are also conducted creatively through space-time coding digital metasurfaces, for example, implementing efficient harmonic control for far-field radiation or near-field distribution [36, 37], manipulating the nonreciprocal effects dynamically for frequency conversion [38–40], realizing smart Doppler cloaks in the microwave domain for self-adaptive anti-Doppler shift [41, 42], executing dynamic and arbitrary polarization syntheses [43, 44], extracting the accurate electromagnetic parameters of complex lossy dispersive materials [45], and more importantly, constructing the minimalist or secure wireless communication architectures through space- and frequency-division multiplexing [46–48].

For applications in radar detection, high-resolution imaging, and secure communication, low sidelobe level (SLL) is an indispensable factor for the designed radiation beams, in order to reduce the coupling interference with other components in the integrated system and minimize the possibility of being detected by the potential adversaries [49]. Due to the difficulty in implementation of multiple-bit phase control, conventional programmable metasurfaces suffer a relatively high SLL. In this manuscript, we design and fabricate a 1-bit transmissive programmable metasurface and overcome its drawback in high SLL through employing a time modulation strategy. After the periodic time modulation, the harmonics are generated in each metasurface unit, and the phase of the first-order harmonic can then be dynamically manipulated by modulation sequences applied to corresponding unit. Through the high-speed modulation of the real-time periodic coding sequences on the metasurface by the programmable bias circuit along with the high-accuracy field-programmable gate array (FPGA), the equivalent phase shift accuracy to each metasurface unit can be improved and the SLLs of the metasurface are thus reduced correspondingly. The time-modulated design strategy is verified both numerically and experimentally, and the transmissive programmable metasurface could obtain an aperture efficiency over 34% and reduced SLLs of about −20 dB, much superior to the conventional programmable metasurfaces. The proposed design could offer a novel approach of a programmable metasurface framework in low sidelobe beam scanning for radar detection and secure communication applications.

2. Time-Modulated Metasurface Theory

The overall schematic and architecture of the time-modulated transmissive programmable metasurface is illustrated in Figure 1. A sinusoidal signal is passing through the transmissive metasurface, and harmonic beams with different frequencies are stimulated and pointing in different directions after implementing periodic time modulation. To improve the equivalent phase shift accuracy and reduce the relative SLLs, time-modulated theory is introduced and combined with the designed 1-bit transmissive programmable metasurface through the high-speed modulation of the real-time periodic coding sequences by the programmable bias circuit along with the FPGA, Xilinx Kintex-7.

Consider a 1-bit transmissive programmable metasurface with \(M \times N\) elements, and the horn antenna is exploited to transmit and receive RF signals with the carrier frequency \(F_c\). The unit spacing along both \(x\) and \(y\) axes is \(D\), and the horn antenna is located on the \(z\) axis with the coordinate \([0, 0, -F]\) in a three-dimensional Cartesian coordinate system, where \(F\) is the focal length of the feed phase center.

Assume that the transmissive metasurface works on the receiving state, and the far-field incident RF signal is incident on the metasurface from the direction \((\theta, \phi)\). The reconfigurable metasurface unit is composed of two patches placed on the front and back sides. The RF signals are received by the front patch and radiated out on the patches on the back, where the signal phases could be dynamically controlled through the 1-bit phase modulator using PIN diodes. The horn antenna receives the power from all the patches on the back layer, which is related to the direction of vector \((\theta_{m,n}, \phi_{m,n})\) and distance \(l_{m,n}\) from the \((m, n)\)th patch on the back side of the metasurface to the horn antenna. Here,

\[
l_{m,n} = \sqrt{\left(m - \frac{M + 1}{2}\right)^2 D^2 + \left(n - \frac{N + 1}{2}\right)^2 D^2 + F^2}. \quad (1)
\]

Assume that the patterns of the patches on both sides are \(u_j(\theta, \phi)\) and \(u_b(\theta, \phi)\), respectively. The loss and phase shift of the \((m, n)\)th patch caused by the distance \(l_{m,n}\) are \(a(l_{m,n})\) and \(exp(j2\pi l_{m,n}/\lambda)\), correspondingly. The array factor of the transmissive metasurface on the receiving state is thus theoretically modeled as

\[
AF(\theta, \phi) = u_j(\theta, \phi) \times \sum_{m=1}^{M} \sum_{n=1}^{N} u_b(\theta_{m,n}, \phi_{m,n}) \times a(l_{m,n}) \times \exp(j2\pi l_{m,n}/\lambda + j\pi\omega_{m,n}), \quad (2)
\]

where \(\omega_{m,n}\) is the programmable weight and selected from the binary states \(\{0, 1\}\) and \(d_{m,n}\) is the distance from the \((m, n)\)th patch antenna on the front layer to the plane
where $F_p = 1/T_p$ is the modulation frequency and $\alpha_{1,1,k}$ is the Fourier coefficient corresponding to the $k^{th}$ harmonic component:

$$
\alpha_{1,1,k} = \frac{1}{T_p} \int_0^{T_p} g_{1,1}(t)e^{-j2\pi k F_p t} dt = \begin{cases} 0, & k \in \text{even}, \\ -\frac{2j}{nk}, & k \in \text{odd}. \end{cases} 
$$

Assume that the received signal in the $(1,1)^{st}$ unit is $A_0 e^{j2\pi F_0 t}$. After modulation, the output can be written as

$$
S_{1,1}(t) = A_0 \sum_{k=-\infty}^{\infty} \alpha_{1,1,k} e^{j2\pi (F+cF_p)t}.
$$

Referring to (7), we see that only odd harmonic components appear after the periodic modulation and symmetric spectra with opposite harmonics exist on both sides of the carrier frequency $F_c$. Among all existing harmonic components, the $\pm 1^{st}$ ones are the strongest. Therefore, we choose the $+1^{st}$ harmonic component to implement pattern synthesis. However, the $-1^{st}$ harmonic component may cause a power waste even interference to other electronic systems. The suppression of the $-1^{st}$ harmonic component requires a more complicated structure with 2-bit phase shift units. However, through the power suppression in other harmonic components, the gain of the time-modulated metasurface phase array can be higher than the one without modulation, owing to the effective improvement on the aperture efficiency caused by the higher phase shift accuracy.

According to (7), if a sinusoidal signal with the amplitude $A_0 = 1$ is input to the modulation unit, the output signal would contain the $+1^{st}$ harmonic component with the amplitude $2\pi$. The modulation sequence applied to the $(m,n)^{th}$ unit is $U_{m,n}(t) = U_{1,1}(t - \tau_{m,n})$, where $\tau_{m,n}$ is the delay of $U_{m,n}(t)$ corresponding to $U_{1,1}(t)$. According to the properties of the Fourier series, $U_{m,n}(t)$ can be unfolded by

$$
U_{m,n}(t) = \sum_{k=-\infty}^{\infty} \alpha_{m,n,k} e^{j2\pi k F_p t},
$$

$$
\alpha_{m,n,k} = \alpha_{1,1,k} e^{-j2\pi k F_p \tau_{m,n}}.
$$

We see that all the time-modulated reconfigurable units would generate harmonic components at $F_c + kF_p$. For the $k^{th}$ harmonic component, the phase difference between the $(m,n)^{th}$ unit and the $(1,1)^{st}$ one is decided by time delay $\tau_{m,n}$. Therefore, the phase shifts of the $+1^{st}$ harmonic components for all the metasurface units can be controlled parallelly by setting different time delays $\tau_{m,n}$ through the FPGA.

According to (4) and (9), after time modulation, the transmissive metasurface array factor of the $k^{th}$ harmonic component can be deduced as
\[ A F (\theta, \phi) = \sum_{m=1}^{M} \sum_{n=1}^{N} \alpha_{m,n} \frac{\cos \theta \cos \theta_{m,n} e^{i2\pi(l_m+n_n)/\lambda}}{l_{m,n}^2}. \]  

(11)

By substituting (7) and (9) into (11), the array factor of the +1 \(^{\text{st}}\) harmonic component can be rewritten as

\[ A F_{+1} (\theta, \phi) = \sum_{m=1}^{M} \sum_{n=1}^{N} -2j \frac{\cos \theta \cos \theta_{m,n} e^{i2\pi[(l_m+n_n)/\lambda]-F_p t_{m,n}}}{l_{m,n}^2}. \]  

(12)

Next, the phase shift accuracy for the metasurface under time modulation would be discussed thoroughly. For the FPGA device used in the programmable metasurface, there is a system clock frequency \(f_{\text{clk}}\), which defines the minimum time interval as \(1/f_{\text{clk}}\) in all programs. That is, all the delays generated by the FPGA are an integer multiple of \(1/f_{\text{clk}}\). Therefore, the phase shift resolution of the period time modulation is \(\Delta \phi = 2\pi F_p f_{\text{clk}}\). For example, if the system clock frequency of the FPGA is 64 MHz and the modulation frequency is 1 MHz, the equivalent phase shift accuracy under time modulation would be equal to that of a 6-bit digital phase shifter. If the required phase shift is \(\pi/4\), the time delay of the modulation sequence should be set as 8 times of the FPGA system clock period. It is worth noting that the transmissive metasurface would operate at \(F_t + F_p\) through time modulation, and the frequency stepping among the harmonic components is \(2F_p\). Therefore, in order to avoid aliasing, the bandwidth of the received signal should be less than \(2F_p\).

Considering the designed 1-bit transmissive metasurface with 20 \(\times\) 20 units working at 7.5 GHz, the unit spacing along both \(x\) and \(y\) axes is \(\lambda/2\), and the feed horn antenna is placed on the \(z\) axis with the coordinate \([0, 0, -5a]\). The exploited pattern synthesis method is to generate an equiphase surface perpendicular to the incidence direction. By substituting the above parameters into (4), the array pattern could be rewritten as

\[ A F(\theta, \phi) = \sum_{m=1}^{M} \sum_{n=1}^{N} \cos \theta \cos \theta_{m,n} \frac{e^{i2\pi(l_m+n_n)/\lambda}}{l_{m,n}^2} \times e^{i(2\pi(l_m+n_n)/\lambda)} e^{i\pi (d_{m,n})}. \]  

(13)

Assume that the required phase for the \((m, n)\) \(^{\text{th}}\) unit is \(\phi_{m,n}\) in order to generate equiphase surface perpendicular to \((\theta, \phi)\). For the situation without time modulation, \(\omega_{m,n}\) in (13) is selected from the binary states \(\{0, 1\}\) and decided by

\[ \omega_{m,n} = \left[ \frac{\phi_{m,n} - (2\pi(l_m+n_n)/\lambda), 2\pi)}{\pi} \right]. \]  

(14)

where \([\cdot]\) is the floor function.

For the situation with time modulation in our study, the system clock frequency of the FPGA is 64 MHz and the modulation frequency is 1 MHz. Therefore, the phase shift accuracy is equivalent to 6-bit at the +1 \(^{\text{st}}\) harmonic component. The required phase shift \(\phi_{m,n}\) is mapped to the time delay \(\tau_{m,n}\) between the modulation sequence applied to the \((m, n)\) \(^{\text{th}}\) unit and that of the reference unit. Thereafter, \(\tau_{m,n}\) is generated by the control FPGA, and the number of delays over the system clock can be calculated by

\[ \omega_m = \left[ \frac{25 \times \text{mod} (\phi_{m,n} - (2\pi(l_m+n_n)/\lambda), 2\pi)}{\pi} \right]. \]  

(15)

As described above, in the pattern synthesis with time modulation, \(\omega_{m,n}\) defines the approximate ratio between \(\tau_{m,n}\) and the system clock period.

In order to validate the effectiveness of the proposed time-modulated transmissive programmable metasurface for low SLL beam scanning, the normalized 2D patterns both with and without time modulation are calculated based on the above theory and examined further with the measured results of the fabricated transmissive programmable metasurface for four scanning directions, including \((0^\circ, 0^\circ)\), \((15^\circ, 0^\circ)\), \((30^\circ, 0^\circ)\), and \((45^\circ, 0^\circ)\). Detailed procedures along with the results are provided in the following section.

3. Time-Modulated Metasurface Design and Verification

The established transmissive programmable metasurface is designed by combining a receiving patch, a transmission patch, and a 1-bit phase modulator in a single reconfigurable unit. The presented 1-bit transmissive reconfigurable unit is constructed by four metal layers along with two substrates and one bonding film, as shown in Figure 2. The two substrates are Rogers RO4003C (dielectric constant of 3.55, loss tangent of 0.0027) with a thickness of 1.524 mm, while the middle bonding film layer is Rogers 4450F (dielectric constant of 3.52, loss tangent of 0.0044) with a thickness of 0.203 mm. The metal layers include, from top to bottom, the receiving layer, the bias layer, the ground, and the transmission layer. The transmission layer is designed with an elliptical patch integrated by an O-slot and two PIN diodes, while the supernatant receiving layer is with the same-size elliptical patch integrated by a U-slot, and both layers are connected at the center by a metallized via-hole through the ground for energy transferring. Besides, the receiving layer is connected with two crescent-shaped distributed capacitors in the bias layer through two symmetric distributed via-holes for biasing purposes, while the transmission layer is connected with the ground plane in a similar way. To choke the high-frequency signals ulteriorly, the bias line is designed with a very narrow linewidth, which is placed very close to the ground and integrated with two symmetric zigzag inductances along with the distributed capacitors.

The 1-bit phase resolution of the transmissive metasurface unit is obtained by integrating two PIN diodes with an antisymmetry configuration for current inversion, thus achieving the uniform amplitudes but inverse phase states
Figure 2: Topology and property of the transmissive programmable metasurface unit: (a) three-dimensional view, (b) receiving layer, (c) bias layer, and (d) transmission layer (item: mm). Simulated scattering coefficients for both coding states: (e) magnitude and (f) phase.

Figure 3: Fabricated time-modulated transmissive programmable metasurface. (a) Front view and (b) vertical view of the metasurface prototype. (c) Experimental environment and measurement setup in the microwave anechoic chamber.
for the two programmable states. The PIN diode, MACOM Flip Chip MA4FCP305 [50], is chosen to acquire lower transmission losses within the designed band and modeled by the equivalent lumped components for the two biasing states. For positive biasing, the series resistor $R_{ON} = 2.1 \, \Omega$ and inductor $L_{ON} = 30 \, \text{pH}$ are adopted for the PIN diode, while for negative biasing, the series capacitance $C_{OFF} = 0.05 \, \text{pF}$ and inductor $L_{OFF} = 30 \, \text{pH}$ are employed for the PIN diode.

The central operation frequency of the transmissive metasurface unit is designed at 7.5 GHz. The numerical simulation is carried out with the help of the commercial software package CST Microwave Studio by using unit cell boundary conditions along with the Floquet port excitations. Figures 2(e) and 2(f) give the simulated S parameters of the unit in magnitude and phase for both $\pi$ and 0 states. For the $\pi$ state with PIN diode I on and PIN diode II off, the transmission coefficient is greater than $-1 \, \text{dB}$ from 7.169 to 7.791 GHz. For the 0 state with PIN diode I off and PIN diode II on, the transmission coefficient is greater than $-1 \, \text{dB}$ from 7.175 to 7.805 GHz. The phase difference of the two programmable states agrees well with 180° within the designed frequency band. Besides, the radiation characteristics of patches on both sides of the metasurface unit are also simulated, which have a gain of about 6 dBi and a beamwidth over 90°.

To further validate the actual working performance of the proposed time-modulated transmissive programmable metasurface for low sidelobe beam scanning, the metasurface prototype with $20 \times 20$ units was manufactured based on the designed unit, with a unit spacing of $d = \lambda/2$ along both $x$ and $y$ axes. A pyramid horn feeder is positioned in the central axis of the metasurface with a focal length of $F = 20 \, \text{cm}$ to illuminate the overall metasurface aperture, and the steering logic board is connected with the metasurface through flexible flat cables (FFCs). The metasurface along
with the horn feeder and the steering logic board is all mounted onto a designed black acrylic bracket, as shown in Figure 3. Each metasurface unit is connected and modulated with the steering logic board through the bias layer network. The FPGA, Xilinx Kintex-7, is adopted as the host processing system in order to regulate the programmable signals in parallel for all 400 PIN diodes of the metasurface within the assigned clock signals, according to the optimized time-varying coding sequences. The reconfiguration speed of the proposed time-modulated transmissive programmable metasurface depends critically on the minimum switching speed of the PIN diode employed, which is about 20 nanoseconds according to the data sheet [50].

The experimental environment and measurement setup in the microwave anechoic chamber are also presented in Figure 3. A horn antenna transmits the single-frequency signal at 7.5 GHz stimulated by the signal source from the far field. The transmissive programmable metasurface is fastened on an electronic-controlled turntable, and the power patterns were recorded by the spectrum analyzer. Firstly, the programmable metasurface is operating with time modulation. The main clock frequency of the FPGA on the control board is set as 64 MHz with modulation frequency of 1 MHz, resulting in an equivalent phase shift accuracy of 6 bits. The pattern synthesis method is to generate an equiphase surface perpendicular to the desired beam direction, and the weight \( \omega_{m,n} \) is calculated by (15) and then translated to the delay of the modulation sequence compared to the reference in programs. As for the situation without time modulation, the weight \( \omega_{m,n} \) is calculated by (14).

The measured normalized 2D radiation patterns both with and without time modulation are plotted and compared with the calculated results based on the above time-modulated metasurface theory, as shown in Figure 4. It is observed that good-shaped directional pencil beams for the four scanning directions, including \((0°,0°)\), \((15°,0°)\), \((30°,0°)\), and \((45°,0°)\), are obtained with very high pointing stability. The main lobes of the scanning beams with and without time modulation are nearly the same, while the SLLs would decrease remarkably by using time modulation. The measured SLLs are lower than −12 dB for the programmable metasurface without time modulation, while that with time modulation would be lower than −20 dB for the scanning beams, reaching a −10 dB decrease on average. The measured results are basically coincident with the calculated predictions, and the relatively higher SLLs of the measured patterns are mainly caused by the assembly error along with the estimation error of the metasurface patch pattern. The beamwidth of the main lobes would be broadened as the scanning angle increases and the SLLs of the metasurface also have a little increase but still remain lower than −20 dB for the scanning beams through time modulation. The maximum measured gain of the metasurface with time modulation is over 26.31 dB with an aperture efficiency in excess of 34%, while a decreased aperture efficiency lower than 30% would be obtained for the programmable metasurface without time modulation. The aperture efficiency is improved because of the high phase accuracy created with time modulation, along with a higher energy ratio achieved for the main lobes through sidelobe suppression.

In order to further highlight the intrinsic advantage of the proposed design, a comparison of the overall performance for the proposed time-modulated transmissive programmable metasurface with some representative published works is tabulated in Table 1. Owing to the equivalent 6-bit phase accuracy created through time modulation, an increased aperture efficiency over 34% along with the reduced SLLs of about −20 dB could be obtained, which has definite improvements when compared with the conventional programmable metasurfaces without time modulation.

4. Conclusion
In summary, we have presented a time modulation strategy for increasing the phase shift accuracy of a 1-bit transmissive programmable metasurface and decreasing the SLLs of the scanning beams. Numerical simulations were provided, and a 20 × 20-unit transmissive programmable metasurface was fabricated and measured to validate the effectiveness of the proposed strategy. Through the high-speed modulation of the real-time periodic coding sequences on the metasurface by the programmable bias circuit, an improved 6-bit phase shift accuracy was achieved at the +1st harmonic component for the time-modulated metasurface. We demonstrated that the aperture efficiency is improved along with the SLLs reduced owing to the high phase accuracy created through time modulation. The time-modulated transmissive programmable metasurface could obtain a measured gain over 26.31 dB along with an aperture efficiency over 34% and reduced SLLs of about −20 dB. The proposed design could offer a novel approach of a programmable metasurface framework in low sidelobe beam scanning for prospective radar detection and secure communication applications.

Data Availability
The data used to support the findings of this study are available within the article and the supplementary materials.

<table>
<thead>
<tr>
<th>Mode of operation</th>
<th>Phase resolution</th>
<th>Gain</th>
<th>SLL</th>
<th>Aperture efficiency</th>
</tr>
</thead>
<tbody>
<tr>
<td>Reflection [19]</td>
<td>1-bit</td>
<td>22.5 dB</td>
<td>-10.0 dB</td>
<td>22.2%</td>
</tr>
<tr>
<td>Folded reflection [21]</td>
<td>1-bit</td>
<td>22.7 dB</td>
<td>-8.0 dB</td>
<td>14.8%</td>
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<tr>
<td>Transmission [26]</td>
<td>1-bit</td>
<td>27.0 dB</td>
<td>-11.0 dB</td>
<td>17.5%</td>
</tr>
<tr>
<td>Time-modulated transmission (this work)</td>
<td>Equivalent 6-bit</td>
<td>26.3 dB</td>
<td>-20.0 dB</td>
<td>34.0%</td>
</tr>
</tbody>
</table>
Raw data are available from the corresponding authors upon reasonable request.

Conflicts of Interest
The authors declare that there is no conflict of interest regarding the publication of this article.

Authors’ Contributions
All authors contributed equally to the writing of the manuscript.

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Supplementary Materials
Supplementary Materials Figure S1: simulated 3D polar power patterns with and without time modulation for scanning direction (20°, 0°). (a) Without time modulation and (b) with time modulation. Figure S2: measured power patterns with equivalent 2-bit, 4-bit, and 6-bit phase shift accuracy. (Supplementary Materials). (Supplementary Materials)

References


